

# Lakshay Chauhan

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## EDUCATION

### Indraprastha Institute of Information Technology (IIIT) Delhi

Bachelor of Technology in Computer Science and Engineering (CSE)

July 2021 – June 2025

## WORK EXPERIENCE

### Member of Technical Staff

July 2025 – Present

ZL Technologies, Inc | [Website](#)

Hyderabad, India

- Contributed to the development of ZLUA, a core component of ZL Tech's unified cloud-native data governance platform, used by Fortune 500 enterprises to manage unstructured data at scale.
- Designed and implemented backend features using Java, C#, JSP, and XML, integrating with Apache Tomcat, Maven, Tika, and Ant for deployment and content processing.

### Research Assistant | [GitLab Project Organization](#)

May 2024 – May 2025

Networks and Systems Security Lab - IIITD | [Website](#)

Dehli, India

- Objective:** Address quality degradation in video streaming applications when fallback from QUIC to TCP occurs due to UDP blocking.
- Solution:** Implemented a mechanism to send notifications in QUIC packets to anticipate UDP blocking. Server logs the kernel congestion window state to be used in the upcoming TCP connection. Improved user experience by avoiding TCP slow start, thus preventing buffering during fallback.

## PROJECTS

### Minimal Compiler Infrastructure 'irl' | [Repository](#)

May 2024 – Present

- Key Skills:** Compiler Infrastructure and Optimization, Systems Programming, Rust, FASM, WASM, Graphviz
- Developed a minimal compiler infrastructure inspired by LLVM. It converts source code into an optimized control flow graph (CFG) and translates it into target-specific assembly code.
- Implemented CFG optimization passes, such as identifier validation, constant folding, and CFG simplification, ensuring high-performance code generation.

### Exploring Compiler Optimizations 'cop' | [Repository](#)

Feb 2024 – May 2024

- Key Skills:** Compiler Optimization, Compiler Passes, Garbage Collectors, Type Safety, LLVM, C, C++
- Developed a null pointer dereference detection compiler pass, a conservative garbage collector for C implementing a bump allocator, and spatial & weak type safety mechanisms by preventing out-of-bounds pointer accesses and disallowing the storage of invalid addresses.

### Task-Based Parallel Runtime Library with 'quill' | [Repository](#)

Jan 2025 – Apr 2025

- Key Skills:** Parallel Programming, Runtime Systems, C++, Multithreading, NUMA, Performance Optimization
- Designed and implemented a task-based parallel runtime library focused on efficient management of concurrent tasks using a worker-based model to minimize synchronization overhead.
- Developed multiple custom runtime systems including async-finish, energy-efficient, numa-aware, receiver-initiated, and trace-and-replay runtime to explore scalability, energy efficiency, and memory locality across heterogeneous hardware.

## TECHNICAL SKILLS

**Languages:** C, C++, Rust, Assembly, Java, Kotlin, JavaScript, Python, Bash, Haskell

**Frameworks:** PyTorch, Django, ReactJS, Numpy, Pandas, Tauri, LibGDX, Raylib

**Developer Tools:** Emacs, Linux, Git, GDB, Markdown, Google Cloud Platform, OpenLiteSpeed, SQLite3

**Technical Electives:** Data Structures & Algorithms, Operating Systems, Cryptography, Database Management, Computer Security, Computer Networks, Compilers, Machine Learning, Natural Language Processing

## AWARDS

Awarded **Summer Undergraduate Research Fellowship** in 2023 by IRD-IIITD for the project “Utilizing ultrasonic distance sensors as a mapping tool to design user-friendly CST”.

Awarded **CHANAKYA Fellowship** by **iHub Anubhuti Foundation** in 2024 for the project “A Unified Approach to User Emotion Detection through Emojis and Textual Analysis”.